## **Resolver Input Module**

M/N 57C411

Instruction Manual J-3640-1



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#### WARNING

THIS UNIT AND ITS ASSOCIATED EQUIPMENT MUST BE INSTALLED. ADJUSTED, AND MAINTAINED BY QUALIFIED PERSONNEL WHO ARE FAMILIAR WITH THE CONSTRUCTION AND OPERATION OF ALL EQUIPMENT IN THE SYSTEM AND THE POTENTIAL HAZARDS INVOLVED, FAILURE TO OBSERVE THESE PRECAUTIONS COULD RESULT IN BODILY INJURY

#### WARNING

INSERTING OR REMOVING THIS MODULE OR ITS CONNECTING CABLES MAY RESULT IN UNEXPECTED MACHINE MOVEMENT. TURN OFF POWER TO THE MACHINE BEFORE INSERTING OR REMOVING THE MODULE OR ITS CONNECTING CABLES. FAILURE TO OBSERVE THESE PRECAUTIONS COULD RESULT IN BODILY INJURY.

#### CAUTION

THIS MODULE CONTAINS STATIC-SENSITIVE COMPONENTS. CARELESS HANDLING CAN CAUSE SEVERE DAMAGE.

DO NOT TOUCH THE CONNECTORS ON THE BACK OF THE MODULE. WHEN NOT IN USE, THE MODULE SHOULD BE STORED IN AN ANTI-STATIC BAG. THE PLASTIC COVER SHOULD NOT BE REMOVED. FAILURE TO OBSERVE THIS PRECAUTION COULD RESULT IN DAMAGE TO OR DESTRUCTION OF THE EQUIPMENT.

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## 1.0 INTRODUCTION

The Resolver Input Module is used to input the angular position of a resolver rotor to the DGS 5000 system. The module error des 12-bit resolution of an error version of the resolution counter. The resolver position may be sampled from 500 microsenones to 32.767 seconds. An external strible input is provided to permit synchronization of the DCS 5000 system to an external event. The module can be programmed to interrupt on every sample.

Typically, this module is used to input rotary shaft obsition for the purpose of determining shaft position or velocity.

This manual describes the functions and specifications of the module. It sitso induces a detailed overview of installation and servicing procedures, as well as examples of programming methods.

Relates subjections that may be of interest:

- J-2611 DOS 5000 PRODUCT SUMMARY
- JR3500 DCS 500D ENHANGED BAS GLANGUAGE INSTRUCTION MANUAL
- J-3601 DCS 5000 CONTROL BLOCK ANGUAGE
   INSTRUCTION MANUAL
- J-3802 DCS 5000 (ADD R LOGICLANGUAGE INSTRUCTION MANUAL
- J-3529 DC5 5000 REMOTE FO INSTRUCT ON MANUAL.
- J-3830 DCS 5000 PROGRAMMING TERMINAL INSTRUCTION MANUAL
- J-3635 DDS 5000 PROCESSOR MODULE INSTRUCTION MANUAL
- IEEE 518 GUIDE FOR THE INSTALLATION OF ELECTRICAL EQUIPMENT TO MINIM ZE ELECTRICAL NOISE INPUTS TO CONTROLLERS FROM EXTERNAL SOURCES

## 2.0 Mechanical/Electrical Description

The following is a description of the taceptate LEDs field termination connectors, and electrical characteriatics of the teld connections.

## 2.1 Mechanical Description

The input module is a printed circuit coard essembly that plugs into the backplane of the DCS 5000 rack. It consists of the printed circuit board, a faceplate, and a protective enclosure. The 'aceptate contains tabs at the top and bottom to simplify removing the module from the rack. Module cimensions are fisted in Appendix A.

The faceplate of the module contising a female connector socket and 5 LED indicators for module atsrue, including one light that incidenes when the board is operational (on) or matfunctioning (off).

Input signals are brough: into the module via a multiconductor cable (M.N. 97G878) see Adpend v D). One end of this cable attaches to the faceplate connector, while the other end of the cable has stake on connectors that attach to a terminal locard for casy field wining. The faceplate connector socket and cable blug are keyed to prevent the cable from being plugged into the wrong module.

On the back of the module are two edge connectors that attach to the system backplane.

### 2.2 Electrical Description

The input module contains a backing resolvento-digital converter that produces a 12 bit digital number proportional to one electrical revolution of a resolver. The digital position may be sampled in one of two ways. The most common method is to specify the sampling period. The period may range from a low of 500 microseconds to a high of 32.7675 seconds, in increments of 500 microseconds.

The second method is to sample the position when an external event occurs. This method is useful when it is necessary to synchronize the DCS 5000 with the occurrence of a particular event. Using an external scobe input is a simple method of synchronizing your spip cation software to the exact obsilion of an object when an external event occurs. See ligure 2.1 for details about the effective disposer at cation impedance of the external etrobe input orbuit. Because of the high input impedance of the etrobe input the device onlying the input mus, have low teskage. See ligure 2.2.

The include can be ping a nime to generate an interrupt whenever it does a periodic earning. This mode ellows you to synchronize task execution with the conversion of new data. The converted data will be leiched when the interrupt is generated.

The module contains a 2-bit electronic counter that can ocurt a total of 4 electrical resolver revolutions. This 2-bit counter is contained in the most significant two bits of resolver position, registers 0 and 1. This counter is reset whenever power is turned on to the system or a heard reset command occurs.

The module produces si26 voltims 2381 Hertz sine wave reference output signal which is capable of oriving a 400 ohm load. This reference signal is transformer isolated and short-dirout protected through a current limitar. The module also receives 11.8 voltims sine and cosine signals from the resolver, as well as the 26 voltims reference.

There are 5 LEDs on the taceptate of the module. The top LED, Isbeled, JIRECTION, indicates the direction of rotation of the resolver. When it is on the resolver is rotating clockwise. The next LED, Isbeled -DBK OK, incicates that the resolver is connected to the module. The next LED, Isbeled: CGLK OK, incicates that the common clock is on. The routin LED, abeled IPS OK, incicates that the isclated power supply a working. Finally, the cottem LED, acceled CK: indicates whether the common clock is on and the isclated power supply is functional. See figure 2.3.



Figure 2.1 - External Strobe Input Circuit



Figure 2.2 - Low Leakage Requirement for Devices Driving Strobe Input



Figure 2.3 - Module Esceptale

## 3.0 INSTALLATION

## 3.1 Wiring

The installation of wiring should conform to all applicable ordes.

To mouse the possibility of cleaning indisc interfering with the properoperation of the control system, exercise care when installing the widing from the system to the external devices. For detailed recommendations, refer to IEEE 518

You should use twisted pair (2 twists per inch) wiring to/hom the roso ver.

## 3.2 Initial Installation

Use the following procesure to install the module:

#### WARNING

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- Stap 1 Remove power from the system. Power to the rack as well as all power to the wring leading to the module should be off.
- Step 2 Take the module out of its shipping container. Take the module out of the anti-static bag. Be ceretul not to touch the connectors on the back of the module.
- Step 3. Insert the module into the desired slot in the rack. Refer to figure 3.1. Use a screwdriver to secure the module rite the slot.

![](_page_12_Figure_12.jpeg)

Figure 3.1 - Reck Slot Numbera

- Step 1. Mount the terminal strip (from cacle assembly M/N 57C373) on a panel. The terminal strip should be mounted to allow easy soccess to the sprew terminals. Be sure that the terminal strip is close enough to the rack so that the cable will reach between the terminal strip and the module. The cable assembly is approximately 60 inches long.
- Step 5. Attach the resolver but leave the mechanical coupling between the resolver and the motor unconnected.

Easten the field wires from the resolver to the terminal strip. Typical field connections are shown in figures 3.2 and 3.3.

Use twietec-pair wire, connected as shown, for the cabling between the resolver and the terminal stric in the control enclosure. Recommended twistec-pair wire is Beloen 1 9497 cable or equivalent. Maximum operating cable length is dependent upon the type of cable you use.

![](_page_13_Figure_4.jpeg)

![](_page_13_Figure_5.jpeg)

Figure 3.2 - Typics) M/N 57G360 Readiver Field Connections

![](_page_14_Figure_0.jpeg)

Figure 3.3 Typical M/N 57C361 Resolver Field Connections

Step & Insert the cable assembly is (M/N 57C373) field terminal connector into the mating half on the module. Use a screwdriver to secure the connector to the module.

> Note that both the module and the terminal ship connector are equipped with "keys". These keys should be used to prevent the wrong cable from being connected to the module in the event that the connector needs to be removed for any reason and then realtached later

> At the time of installation, rotate the keys on the module and the connector so that they can be connected together securely. It is recommended that, for modules so ecuipced, the keys on each succeeding module in the rack be rotated one postation to the right of the keys on the preceding module.

If you use this method, the keys on a particular connector will be positioned in such a way as to fit tagether only with a specific module, and there will be ittle chance of the wrong connector being attached to a medule.

- Step / Check the wiring and be sure all connections are tight.
- Step 8 With the resolver mechanically disconnected from the motor, turn on power to the rack. Use an oscilloscope to test the alternation the cosine algobia from the resolver.
   These signals, measured at the terminal strip, should be all sine wave of approximately 33.4 Vo-p (11.8 Vrmall) (10%).
- Step 9 Verify the installation by using the Programming Executive Software. Befor to the AutoMax Programming Executive Manual (J-3630 or J-36304) for more information

Use the FO MON TOR function for local I/O or remote FO, depending upon where the module is located. Set register 4 to a value of 1. Resp register 2 and verily that bit 15 is set. If it is not set register 3 to a value of 64.

Monitor recisier 0. Verify that it contains numbers proport and to the shell position of the resolver and that the numbers increase as the resolver is rotated clockwise. The direction of rotation can be reversed by switching the polarity of other the sine or the cosine wires. See figure 3.4.

![](_page_15_Figure_1.jpeg)

Figure 3.4 - Changing the Direction of Rotation

- Step 10. If the extensi strobe input is being used, the shaft anould be intered to a fixed position and stopped. The extension strobe input should now be closed. Verify that register 1 contains the same data as register 0.
- Step 11. Turn oll power to the rack. Connect the mechanical coupling between the resolver and the motor. Turn on power to the system.

## 3.3 Module Replacement

#### WARNING

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Use the following procedure to replace simpdule:

- Step 1. Turn off power to the rack and all connections
- Step 2 Use a screwdriver to loosan the screws holding the opmestor to the module. Remove the connector from the module.
- Step 3 Loosen the screws their hold the module in the rack. Remove the module from the slot in the rack.
- Step 4 Flace the module in the anti-static bag, being careful not to touch the connectors on the back of the module. Place the module in the cardboard shipping container.
- Step 5 Take the new module out of the anti-static bag it came in, being careful not to touch the connectors on the back of the module.
- Step 5 Insett the module into the ceatred a of in the rook. Use a somewher to socure the module into the slot.
- Step 7. Attach the field terminal connector (WN s7C373) to the mating half on the module. Make certain that the connector is the proper one for this module (see step 6 in a.2, nitial installation). Use a screwariver to secure the connector to the module.
- Step 8 Turn on power to the rack.

## 4.0 PROGRAMMING

This section describes how the data is organized in the module and provides examples of how the module is accessed by the application software. For more detailed information refer to the DCS 5000 Enhanced BASIC Language Instruction Manual (J-3600).

## 4.1 Register Organization

The Input module contains a total of two 16-bit registers. Registers 0 and 1 contain resolver consition data. The resolver to-algital converter provides 12 bits of resolution. Register 0 is updated with new position information at the rate specified in register 4. Register 1 is updated whenever the EXTERNAL STROBE goes from false to true. These registers are read only. Refer to figure 4.1

![](_page_18_Figure_4.jpeg)

Figure 4.1 Resolver Data Registers

Registers 2 and 3 are the interrupt status and control registers. Both registers contain the same information. Register 2 is read only Register 3 is read/write. If the module is locates in a compte rack, you must read the status from register 2. With the exception of b 16 are bit 12, this register is controlled by the operating system and must not be manipulated by the user. Befer to igure 4.2.

For this module to operate property, the common clock signal must be present on the backplane. The common clock signal is a 1 mbz clock that can be connected to all the 70 modules in the rack. It can be generated from a number of DCS 5000 [/0 modules if this module is to drive the common clock, bit 6 must be set.

![](_page_19_Figure_0.jpeg)

Figure 4.2 - Interrupt Control Registers

Register 4 contains the update period for reading the resolver position. Each count in this register is equivalent to 500 microseconds. The update period may range from 500 microseconds to 32,7675 seconds. Refer to ligure 4.0.

bits	15	14	13	12	11	10	9	8	7	¢i .	5	4	ы	2	1	0
A set loc				1				-		1	1					
eduate -	1.12	- 22	- 2			1.1	inter-	sue:	seam		- X.			- 8		2 -

Figure 4.3 - Hesolver Update Register

## 4.2 Local I/O Definition

Before any application program can be written it is necessary to configure, or set, the definitions of system-wide variables. Let those that must be globally accessible to all tasks. This section describes how to configure the input module when it is located in the same task as the processor module that is referencing it. Refer to figure 4.4.

![](_page_20_Figure_0.jpeg)

Figure 4.4 - Module in a Local Rack

#### 4.2.1 Single Register Reference

Use this method to reference a 16-bit register as a single input. Resolver input data, update period, and interrupt control registers are typically referenced using this method. The symbolic name of each register should be as meaningful as possible:

```
mmm ICDEF SYMBOLIC_NAME% | SLOTHs, REGISTERH()
```

#### 4.2.2 Bit Reference

Use this method to reference individual inputs on the module. Common block alsue and control bits are typically referenced using this method. The symbolic name of each of should be as meaningful as possible.

n then ICDEF SYMBOLIC\_NAME ((SLOT-s: REGISTER-r, BIT-b)

where:

nnnnn - BASIC statement number, This number msy range from 1-52767.

SYMBOLIC\_NAME% - A symbolic name chosen by the user and ending with (%). This indicates an integer data type and all references will access register 11'.

SYMBOLIC NAMEQ - A symbolic insme chosen by the user and ending with (*Q*). This inclustes a boolean cate type and all references will eccess bit number "b" in register "r",

SLOT - Sits number that the module is plugged inte. This number may range from 0.15

RECISTER - Specifies the register that is being referenced. This number may range from 0-1.

B.1 - Lead with Boolsen data types only Specifies the pit in the register that is being referenced. This number may range from C-15.

#### 4.2.3 Examples of Local I/O Definitions

The following statement assigns the symbolic name POS TION is to register 0 of the input meaula located in slot 4:

1020 IOBEF POSITION% SLOTHA, REGISTERHOL

Lite to lowing statement assigns the symbolic name UCLK\_ON@ to bit 6 of register 3 on the input module located in slot 7: 2050 [IODET COLK\_ON@[ISLOT=7, REGISTER=3, BIT=6]

## 4.3 Remote I/O Definition

This section describes how to configure the module when it is located in a rack that is remote from the processor module referencing it. Refer to figure 4.5.

![](_page_22_Figure_0.jpeg)

Figure 4.5 - Module in a Remote Rack

### 4.3.1 Single Register Reference

Use this method to reference an 6-bit register as a single input. Readver input dats and update period redisters are typically referenced using this method. The symbolic name of each register should be as meaningful as possible.

non A COEF SYMBOLIC NAMEX.(MASTER SLOT=n, DRCP=1.3.CT=0.9E3(STER=r)

### 4.3.2 Bh Reference

Use this method to reference individual inputs on the module. Common clock status and control bits are typically referenced using this method. The symbolic name of each of should be as meaningful as possible:

nmm RIODEF SYMBOLIC\_NAMB@[MASTER\_SLOT=m, DROP=d, SLOT=e, REGISTER=(, BIT=b]

where:

remmini - BASIC statement humber, This number may range from 1.32767.

SYMBOL C\_NAME% - A symbolic name chosen by the user and ending with (%). This incicates an integer cats type and sill references will access register 11'.

SYMBOLIC\_NAME(#) - A symbolic name chosen by the user and ending with ((#)). This inclustes a boolean cate type and ell references will access bit number "b" in register "r".

MASTER\_SLOT - Slot number that the master remote I/O module is plugged into. This number may range from 0-15.

DRCP - Drop number of the slave remote //O module that is in the same rack as the input module. This number may range from 1-7.

SLOT - Size number that the module is plugged inte. This number risy range from 0-15

REGISTER Specifies the register that is being referenced. This number may range from 0.4.

B T - Used with boolean data types only. Specifies the bit in the register that is being referenced. This number may range from 0-15.

### 4.3.3 Examples of Remote I/O Definitions

Life to lowing statement assigns the symbolic name SHAF1% to register 0 on the input module located in slot 4 of remote VC erep 3. Unla remote crop is connected to the remote VC system whose insater is located in slot 15 in the master rack:

1020 RIDDEF SHAFT & MASTER\_SLOT=1a, DROP=3, SLOT=4, REGISTER=0]

The to lowing statement assigns the symbolic name GLOK\_EN@ to bit 6 clinecister 3 on the input module located in stol 7 clinemole MO drop 2. This remote drop is connected to the remote MO system whose master is located in stol 6 in the master rack.

2050 RIDD-F CLCR - N@ (MAS FH SLOT=5, DBCP-2 SLCT=7, REGISTER=3, BIT+5]

## 4.4 Reading and Writing Data in Application Tasks

In order for an input meau e to be referenced by application software. It is first necessary to assign symbolic nerves to the physical hardware. This is accomplished with either IODEF or BIODEF statements in the configuration task. Each application program, or task, that wishes to reference the symbolic names assigned to the input module may do so by declaring those names COMMON.

I he frequency with which tasks reed their inputs and write their outputs depends on the language being used. Control block tasks reed inputs once at the beginning of each scan and write outputs once at the end of scan. BASIC tasks read an input and write an output for each reference throughout the scan.

The following is an eventple of a configuration task for the input module:

```
1075
1001
         DESC VELOCIT
10.4
      ICCEPRESCIVES_NS/3.0T=4, REGISTER=0]
ICCEPTERSON (ENDIX SECONDECT)
1005
1015
1021
        commen proce charake
1012
      TODELOC K INVESTIGATION AND STER ALTER AL
HIZD.
1021 Ar.2 . pole a period
1026
1071
     IOBEFUERVE TIMES/SLOT-+ REDISTER-4
(dtp)
       Fluce any add to relieveling makes successful and ender
10:1
10:2
2010 END
```

#### 4.4.1 BASIC Task Example

I his example will reed the readiver input once every second and store the value in the symbol "CURRENT\_VALUE". The readiver position will be sempled every 100 mill seconds.

동안 영상 이상 것이

10.0	COMMON RESOLVER INN COMMON OSLK ENS	HRusolyon data (peripero) Heattmen diock etable
1050	COMMON/UPDATE_INTS	Sulposte perior for realiver conversion
1602	COCAL CURRENT, VALUES,	+Current value of choicing in suc
2000	1020A00_10075 200	<ul> <li>Clisecond conversion</li> </ul>
40.0	COR_1905 = 1.0°	dur von die clock
4001	Place any additional initialization	software here
5000	STARE VERY DECEMBER	
SULD	GUFRENT VALUESS = RESOLVER	155
10000	END	

The symbolic names defined as "CDMMON" reference the inputs defined in the sample configuration task spove. The symbolic name CUBRENT\_VA\_UES is local to the BASIC task and does not have (C) associated with 1. Refer to the UCS 5000 Enhanced BASIC Language instruction Manual (J-3800) for more information.

#### 4.4.2 Control Block Task Example

The following example will read the resolver data every 55 milliseconds and store the inverted value in the symbol (BEADING). The resolvers shaft position will be sampled every 500 microseconds.

1010	COMPACE BESCHAFT, NY-	<ul> <li>Heroker fata (periodic)</li> </ul>
1015	COMMON LACER	(Contracting opportunities
1075	GORMACK LEDALL TIMES.	Autocelle perior, fut re-obert cons-
14.0	그 전 것 같은 가지만, 것이 가 못 것 같이 다.	
1500-	LOCAL READINGS	<ul> <li>Europi regative value of input</li> </ul>
1000		

```
2012
                                                          DEMONSTRATION IN THE REPORT OF THE REPORT OF
                                                                                                                                                                                                                                                                                                                                                        4000 minuserond backersion
C106
                                                            GOLK LNO - THE
                                                                                                                                                                                                                                                                                                                                                       -Limman Juscause
4812
4001
                                                                 Place one additional initialization software here-
4002
1981
                                                                   Sten erers 35 ments
421.
5000
                                                          GALL 3CAN LOOP( TCK3+10)
5010
                                                          CA INTERPRETE DISCOUTE DISCOUTE DIA DISCOUTE
10120
                                                    UN C
```

The symbolic names defined as "COMMON" reference the inputs defined in the sample configuration task above. The symbolic name "READING'S" is deal to the BASIC task and does not have I/C associated with it. Refer to the DCS aCDC Control Block Language Instruction Manual (J 3601) for more information.

## 4.5 Using Interrupts in Application Tasks

The input module supports an interruption the period c resolver-to-digital conversion, interrupts are used to synchronize software tasks with the resolver-to-digital conversion. Conversion rates may be specified from 500 microsecones up to a maximum of 52,7575 seconds in increments of 500 microsecones

In order to use interrupts on the input module, it is necessary to assign symbolic names to the interrupt control register. This is accomplished with ICDEF statements in the configuration task. Note that interrupts cannot be used with modules located in remote racks.

Chly one task may act as a receiver for a particular hardware interrupt. That task should declare the symbolic names assigned to the interrupt control register on the indul module as CCMMON. Once this has been done, any reference to those symbolic names within the task wit reference the bits or register cellned in the configuration task.

Lite to lowing is an example of a configuration task for an input module using interrupts:

```
1007-
1011
        test ver deta
1001
1005
       IODEF REPOLVER INS/3.0T=4. REG STER=0]
       ID THE REPORTER, IN EXTING OF 14, RED STEE, 1].
1015
1012
101.1
        titism grively us and combiding over tissed by the operating systems.
101 -
       DOEF STROPE, STATUSSI GLOT-4, FEG STER-2 EFT-14
10:1
       (5) 1 (CPS) 5 (D1-4, 14 (G51, 18-3))
1010
       100a+ 5 H08a Ad vg/ 600 = 4 Hebits EH=3,88 = 12-
IUTS.
1025
        dominant clock chable.
1021
1022
       DC: DC 8_ N335-01-4, 133-51-4-3, 11-5
1125.
1030
1031
        reservence wersion period
1092
       DOI: LOOK LOOKINGS (LI-4) LOOS LI-4
10.15
1052
        Place oldektic hall configuration statements here.
1061
1052
2012
       CND:
```

This configuration defines all of the information most commonly used on the module. Unused definitions should be deleted by the user.

#### 4.5.1 BASIC Task Example

The following is an example of a BASIC task that handles interrupts from the input module defined in section 4.4.

```
LOCO.
         COMMON III SOLV 4, NY-
                                                     -I devolver dara
1005
         COMMON PRO PH
                                                     -I memori status Nicotata.
1015
         GLAWICH OLLK EVER
                                                     victor mich diode de labler.
         COMMON LIPENTE TIMES
1020

    Resolver conversion time.

         LOCAL RESO, VEB, VALUES,
                                                     All ac the formation
CORE
20.1
           Definent else wersion plaameters
20.2
         UPDATE_TIMES - 1002
2017
                                                     +Epityeri every Biseconds
0000
3001
          The following statement compared the name RESOLVER EVENT of the little light dollned in 190 R/s. The event name of open should
30.2
          be as meaning). I as possible. The watchdog i medutinas 
been write 121 clock toxy 1000 meed. If the time between
3005
1011
           interrupts exceeds has very a several a for wit percedance.
000e-
          and the system will be excepted. For more informatic metanic the DCS 5000 Enhanced EASIC Long, ago Institution Manual (J. 3009).
$3.5
3007
2022
3015
         EVENTINAME=RESOLVER EVEN ;
3011
           INTEFFLIPT STATUS=ISCRK.TIMEOUT=129
4000
1071
           The follow op statement enables common kinds hore tals more e-
1003
           I there is more than one interrupt task in a cleasable the later
40.5
           that enobles commented ek shea dialwayo be the lowest priority.
40.4
           usk
6077
CI DE
         CONDENS: 101
                                                     «Communicace enside
5800
5001
           Place additional initial asi an software here.
20.22
8000
8001
           The readiated statement wand include the task with the external
           event via the internation Task execution will be passended.
10.2
6003
           until the Intellige accure. This task is the highest
5001
           priority lask wait to no execute state time of the
8005
           instrupt, by Locare activation of the
80.5
           Inglical priorits autoritized remain acapenees and
          all higher pricity tasks have executed, all which be nt. If will become active
6007
DOCT
SUCH
8012
         WATCN REBOLVER EVENT.
7015
7001
           The next statements perform the mention require multiple
mer.
/015
         RESOLVER WALLES . FESOLVER NS.
10000 END
```

#### 4.5.2 Control Block Task Example

The following is an example of a control block task that handles interrupts from the input module defined in section 7.4.

```
COMMON RESOLVER. NN
                                              unt color.Fl
10.0
1000
        COMMON ISORY.
                                              I me "Jot clatus & contro-
10.15
       CONNECS OF RUNDAL STREET, NO. 5
                                               Common coccarable
(G3DI
                                              different sourcestern anne-
13.0
       LOCAL RESOLVER WALLEN.
                                              (Read/or to up
20.0
2011
         Define the conversion protocols
2002
0015
        UPDATE TIMEN = 100
                                              HED wait every spin it i becomes
3000
         The following waterpent connects for name 10,000 2018 (1913) I to
1001
1012
         the Interright delines in ISCO'S. The event owner chosen should
5005
         be average including possible. The welchdoor travely real
30.4
         been set to 12 block i des 188 msect. If the time between
         Interningte angeeds into value in severe entrinvilline geglared.
2005
         and the evalent will be approved. For more information referring the
         DCB 5000 En randod EASIC Long age 1 st det all Monaul (a 3800).
30.1
$0.5
3012
       EVENTIN/ME_RESD_VER, EVENT, &
         INTERPOSE AUX DOAN, DO DE 12
10.11
```

```
1002
10011
          the kilowing scalarment exercises common cluck if an intermedical
400.
         If a rere is more than one interrout task in a clossis, the toan
4005
         that enables common abolt sheard always be the lowest priority.
400.1
         Inch
101%
        GOLK END - FUE
48.5
                                               Adominicipet enable.
5000
5011
         Place additional initialization software bere
2012
SIMO
8001
         The just slote run, synchon ses the task with the core tal
         event via the interrupt Task reveal on will be suspended.
6002
5001
         until the interning boots or. If this task is the highest
         prior ty task waiting to associate at the time of the
33814
         interlipt, twil becare acive. If it a lattice
20015
6005
         highest pricity task, it will emain suspended until
5017
         si hidher priotik taxos have esecured in which holds
2005
         1.74 Decompactives
1000-
        CA SCAN LOOPET CHS- 9 EVENT-REVOLVER EVENTS
6015
2012
2011
         The rest starements perform the oten of service to line.
/00L
        CALL PULSE, MULTINPUT- FESOLVER, NK. MULTPUER-16386, 6
70n D
                    तिहत गो। ज उदार सुप्रधान ५१
10020
       IN:
```

## 4.6 Using the External Strobe Input

At the time of the extense event, the resolver poaltion is transferred to register 1, where it will remain until the next event occurs. If your application software is reading the resolver obsilion at a periodic rate (register 0), the difference between register 0 and register 1 represents the amount of travel from the time that the event occurred until the current scan of the application software. A typical application would be detecting the leading edge of an object moving on a conveyor.

End data captures by the strate input will be as accurate as the external device driving the input. Note that when a strate input has occurred, you must reset the input so that another one can occur. I his is secont plianed by writing a 'tri to hit 12 of register 3.

The lollowing is an example of a control block task that handles the strate input from the input module defined in section 4.4

```
1002
      d headlest care.
      COMMON REBOLVER IN EXTS.
1001
                                         Budde reacher data
      COMMON ISORN
                                        Vinternuct status, & conit di
100.5
      COMMON OC K, FNS
401.7
                                        Addresseds and monoclassic
      COMMON LEDGET. THE ST
1022
                                         d descrives conversion time.
      COMMON STROBE STATUSS
1030
                                         entrie w.euSk
1045
      COMMON STREES ACKIN
                                        Histrape at inovietige.
1300
       LOCAL RESOLVER WALVES.
                                        (Recohernalue)
1210
       OC4 914001 0 1118
                                        diam're sinte-
       LOCAL FE TOD DIS ANOLS
1225
                                         d leadest same scattlager.
128.2
       LOCAL EVENT DISTANCES.
                                        Repolicing the free country action
2007
1006
       Define the conversion parameters
1002
301 D
       UPDATE TIMES = 100
                                         Wonself exclusion militacion ta
30101
       EVENTINAME-RESOLVER_EVENTINTERRUPT_STATUS- SORS 5
          120101 2
(U12)
       GOLK L'AQ = 1 OL
                                        -Reformment and park small st
SUUS.
       CALL BOAN LOOP/ TICKB = 0. EVENT = RESOLVER EVENT
601.2
2002
2001
        The next statements calch the occurrence.
/001a
       of the all cheronic results.
7005
      05 TRANSFION A NOT STAFFIN, A
701.2
                        CONTRACTOR DESC 83
ALCO
       STHORE ALKS/= \OTS HUGE HEREINS
```

```
7027
AICS
        The next statement conclusive the character
Aller
        t excludit carrille sociarence of the estimation
1045
        event until dis source
7059
7025 G4 MEST MULTIN METHODOLY LENG &
                          HESET = STROBE RESETVA &
NTAL_WAUE = RESOLVER, M_EXTX, &
                          WHITE IS 1625
                         OFFICE VENT COMMONY
        If all react station per contains, the chain on Inwalled
ALC: N
/05/
        Formitier loss soon antil this soone
       G4 MEST WELL NOT TRESOLVE LINKS &
7042
                          WJLTPLER=18555 4
                          OUTPUT=PERIOD DISTANCES/
7045
7047
        The next statement card also the connection.
1145
        neurod to synchronic with endernal event to
2042
        Lieping and
2003 CA DEFERINCE NRICH FERICE DISTANCES A
                          N ST 2-LY AT D STANG 14 4
                          SUTPLIT=RESOLVER WALLERS
10030 END
```

#### 100.0 2105

## 4.7 Restrictions

This section describes limitations and restrictions on the use of this module.

#### 4.7.1 Writing Data to Registers

Registers 0-2 are read only and may not be written to by the application activare. Attempts to write to them will cause a bus error (asvere system error). The following are exempted from programs that write to the module and should therefore be avoided:

 Referencing the module on the left side of an equal sign in a LET stetement in e control block or BASIC task.

 Beferencing a resolver input as an output in a control block function.

#### 4.7.2 Interrupts in Remote I/O Racks

This module cannot be used in the interrupt mode in a remote rack.

#### 4.7.3 Feedback Element in a Drive System

When this module is used with a resolver in s drive control system, you must incorporate an independent method of determining that this module is actually reading proper motor RPM. It is necessary to determine this because this module is not capable of detecting a ross of feedback in all situations, such as for example, a broken coupling between the motor and resolver.

#### WARNING

LOSS OF, OR OTHERWISE IMPROPER, RESOLVER SIGNAL CAN RESULT IN UNCONTROLLED MOTOR SPEED. PROVIDE AN INDEPENDENT METHOD OF SHUTTING DOWN EQUIPMENT IF THIS SHOULD OCCUR. FAILURE TO OBSERVE THIS PRECAUTION COULD RESULT IN BODILY INJURY AND IN DAMAGE TO, OR DESTRUCTION OF, THE EQUIPMENT.

You must elab determine the maximum sets operating apeed for the motor, connected machinely, and material being processed. Then, either verify that the system la incapable of reaching thet speed, or else incorporate the necessary bardwary software to ensure that this limit will never be exceeded.

#### WARNING

THE PURCHASER IS RESPONSIBLE FOR ENSURING THAT DRIVEN MACHINERY, ALL DRIVE TRAIN MECHANISMS, AND THE WORKPIECE IN THE MACHINE ARE CAPABLE OF SAFE OPERATION AT MAXIMUM SPEEDS. FAILURE TO OBSERVE THESE PRECAUTIONS COULD RESULT IN BODILY INJURY AND IN DAMAGE TO, OR DESTRUCTION OF, THE EQUIPMENT.

## 5.0 DIAGNOSTICS AND TROUBLESHOOTING

This section explains how to troubleshoot the module and field connections.

### 5.1 Incorrect Data

Problem: The data is either always off, a ways on, or different then expected. The possible causes of this error are a module in the wrong slot, a programming error, or a malfunctioning module. It is also possible that the input is either not wired or wired to the wrong device. Use the following procedure to isolate the problem:

Step 1 Verify that the input module is in the correct slot and that the FO cell titlions are correct.

Referenced agrees with the slot number being referenced agrees with the slot number defined in the configuration task. Verify that the register number and the bit number are correct.

For remote I/C installations, also verify that the masterialot, and drop numbers are defined correctly. Refer to the DCS 5000 Remote I/O Instruction Manual (J-3629) for more datailed information on configuring your remote I/C system.

Step 2 Verify that the module can be accessed.

Connect the programming terminal to the system and run the ReSource Software. Use the I/O MONITOR function to display the four registers on the input module. Repeat steps 7 and 8 in section 3.2

Step 3 Verify that the user application program is correct.

Boview the programming examples in sections 4.4, 4.5, and 4.5. Make certain that the VC certations in your configuration task are correct and that the task(s) using this module have declared these variables COV MCN.

Verily that an update period has been written to register 4. Recall that each count is 500 microseconds (.0005 seconds). This value specifies the frequency with which the resolver position will be converted to cigital numbers.

Verily that the common clock has been turned on. The "CCLK OK" LED on the "apoptate of the module should be lit. If the common clock is not present on the backplane, the module will not convert the resolver position to digital values. If the common clock is being generated from this module, remember that bit 6 in register 3 must be set.

Step 4 Vority that the resolver is wired correctly.

Remove power from the system. Disconnect the mechanical coupling between the resolver and the motor.

Confirm that all the terminal strip connections are tight. Refer to figures 3.2 and 3.3. Appendix G also lists the terminal strip connections

Apply power to the rack only.

I' everything is working properly but the cirection of rotation is backwards, it may be reversed by switching the polarity of either the sine or the cosine wires as shown in figure 3.4.

#### Step 5. Verify that the input circuit is working properly.

Connect an exciloscope to the proper points on the terminal strip and confirm that the voltages are correct.

The resolver reference vollage across TB 1 and 2 should be a nominal 26 Vrms.

If it is 86 Virtis, check the jumpers on the form hall ship. One jumper around connect. Bit sho 3. Acother jumper should connect TB 2 and 4. Rotate the readivers shaft and measure the sine voltage (TS 5 and 6) and cosine voltage (TS 7 and 8). Both voltages should range from 0 volta to approximately 11.5 Virtis.

If the resolver reference (TB 1 and 2) is not a nominal 26 Vrms, measure the D-C resistance of the resolver. Disconnect the resolver cable from the terminal scip (TB 1 and 2) and measure the resistance across the disconnected wires.

The resistance should be in the range of 35 to 125 chms. If the resistance is within this range, the input modure is malfunctioning and should be replaced. If the resistance is not within this range, disconnect the cabing from the resolver and measure the resistance directly on the resolver.

It this resistance to reading within the 35 to 100 orm, range, the readiver is operating property. Check the cabling for elposible short. If the resistance to not within the 35 to 100 orm range, the readiver is multiunclioning and should be replaced. If the problem is actil present check the cable for a possible short.

Reconnect the resolver cable at TB 1 and 2.

Remove power from the rack, Reconnect the mechanical occuping between the resolver and the motor. Reapply power to the system.

Step 6. Verify that the barowere is working property.

#### WARNING

INSERTING OR REMOVING THIS MODULE OR ITS CONNECTING CABLES MAY RESULT IN UNEXPECTED MACHINE MOTION. POWER TO THE MACHINE SHOULD BE TURNED OFF BEFORE INSERTING OR REMOVING THE MODULE OR ITS CONNECTING CABLES. FAILURE TO OBSERVE THESE PRECAUTIONS COULD RESULT IN BODILY INJURY.

F all of the proper signals are present and the values are still not correct, the problem lies in the hardware. Verify the hardware functionality by systematically swapping out modules. After each swap, if the problem is not corrected, replace the original item before swapping out the next life  $\tau$ .

To les, local I/O, linst replace the input module. Next, replace the processor module (a). If the problem persists, take all of the modules exceptione processor module and the nput module out of the backplane. If the problem is now corrected, one of the other modules in the rack is malfunctioning. Reconnect the other modules one at a time until the problem response. If none of these tests reveals the problem, replace the backplane.

To test remote I/O, first verify that the remote I/O system is communicating with the drop that conteins the input module being tested. Next, by systematically swapping out modules, determine whether the input module is the only module that is not working.

If more than one module is not working correctly, the problem most likely lies in the remote I/O system. Refer to the the DCS 5000 Remote (\*C instruction Manual (J-3629) for additional information. If the problem does not lie in the system, it probably involves the remote rack.

To test the remote rack, that replace the input module. If the problem persists take all of the modules out of the remote backplane except the slave remote 50 module and the input module. If the problem is new convected, one of the other modules in the rack is mail including. Reconnect the other modules one at a time until the problem resoposes. If the problem proves to be not her in the remote (Claystern her in the remote rack, try replacing the backplane.

## 5.2 Bus Error

Problem: A "31" or "16" appears on the processor module's LED. This error message inclusion that there was a bus error when the system attempted to access the module. The possible causes of this error are a missing module, a module in the wrong slot, or a melfunctioning module. It is also possible that the user has attempted to write to the wrong registers on the module. Use the following procedure to isolate a bus error:

Step 1 Verily that the input module is in the correct slot and that the (/O cell hitions are correct.)

Refer to 1 gure 3.2. Verify that the alot number being referenced agrees with the alot number defined in the configuration task. Verify that the register number is in the range of 0-4.

For remote I/C installations, also verify that the master slotand remote area number are defined connectly

Refer to the DCS 5000 Remote I/O Instruction Manual (J-3628) for more information on configuring your remote I/O system.

Step 2. Verify that the module can be accessed.

Connect the programming terminal to the system and run the ReSource Software. Use the I/O MONITOR function to display the four registers on the input module. If the programmer is able to monitor the inputs, the problem lies in the application software (refer to step 3). If the programmer cannot monitor the inputs, the problem lies in the hardware (refer to step 4).

#### Step 3. Varity that the user application program is correct.

Registers 0 through 2 of the input module cannot be written to. If a SASIC task caused the cus error the error log will contain the statement number in the task where the error occurred. If a control block task caused the error, you will need to search the task for any instances where you write to an input.

Step 4. Verily that the hardware is working correctly.

Verify the hardware functionality by systematically swapping out modules. After each swap, if the problem is not corrected, replace the original tiem before swapping out the next tern.

To test local  $|\langle \Omega \rangle$  replace the input module, the processor modules (s), and finally the backplane.

For remote EO, determine whether the input module is the only module that is not working. If it is not, the problem most likely lies in the remote FO system. Pefer to the DCS 5000 Remote 70 Instruction Manual (J-3629) for additional information. If the problem cose not lie in the remote I/C system, it probably involves the remote rack.

b feet the remote rack, systematically awap out the input module, the slave remote kO module, and finally the backpiene. After each swep in the problem is not corrected, replace the original item before going on to the next swep. If none of these actions correct the problem, troubleshoot the remote kO system.

## 5.3 Interrupt Problems

Problem: No interrupts at all or too many funexpected) interrupts, signified by error codes on the screen. Note that this module must be in the same rack as the processor module that is to receive the interrupts. Go through the following steps first before going on to the more specific troubleshooting steps.

 Verify that the input module is in the correct slot and that the UO definitions are correct.

> Refer to figure 3.2. Verily that the sket number being referenced agrees with the slot number defined in the configuration task.

Verify that the configuration task contains the proper interrupt control definitions. Refer to the example in section 4.6.

Step 2. Verily that the user application program is correct.

Verify that the application program that uses the symbolic names cellined in the configuration task has defined those names as COMMON

Compare your Interrupt teak with the examples given in sections 4.5,1 and 4.5,2. Make certain that the actions shown in the examples are performed in the asme order in your task.

#### 5.3.1 No Interrupts

Problem: The program does not execute, but no error codes are disalayed on the processor module faceplate. If interrupts are never race yes, by the application program and the timeout parameter in the event definition was disabled, the task will never execute.

The watchcog timer for this module should never be cleapled. Before you can determine why the program clic not execute, you must first set the timeout parameter in the event definition. But the program again and proceed to section 5.3.2.

#### 5.3.2 Hardware Event Time-Out

Problem: All tasks in the chassis are stopped and error code "12" appears on the faceplate of the processor module. The interruct has either never occurred or is occurring at a slower frequency than the value aped fed in the timedul parameter in the event definition. Use the following procedure to isolate the problem.

Step 1. Vorily that the timeout value is set correctly.

Check the value specified in the timeout parameter in the event definition. The finit is in taxs. Each tak is equal to 5.5 msec. The timeout value should be at least 2 taks greater than the interrupt frequency. It can reasonably range up to 1.5 times the interrupt frequency.

- Step 2 Verily that the user application program is correct. Beview the examples in section 4.5. Make cortain that common elock has been enabled.
- Stap 3 Verily that the hardware is working correctly.

Systematically awap out the input module, the processor module (s), and the beckplane. After each awap, if the problem is not corrected, reclade the original item before swapping out the next tem.

### 5.3.3 Hardware Event Count Limit Exceeded

Problem: All tasks in the crossis are sinpled and error code " 15" sppears on the tecepiste of the crossisor inclule. A histoware interrupt has occurred but no task is waiting. Use the following procedure to isolate the problem:

Step 1 Verily that the user application program is correct.

Verify that your interrupt response task contains either a "WAIT ON event" or "CAUL SCAN\_LOOP " statement that will be executed. Check carefully to betermine whether a higher priority task is preventing the interrupt response task from running. Make certain that the ordering of your statements agrees with the examples in section 4.5

Step 2 Verily that the bareware is working correctly.

Verily the hardware functionality by systematically swapping out the input module, the processor module (s), and the backplane. After each awap, if the problem is not corrected, replace the original item before swapping out the next item.

### 5.3.4 Illegal Interrupt Detected

Problem: All tasks in the chassis are stopped and error dode "1F" appears on the faceplate of the processor module. A hardware interrupt has occurred but no event has been extined.

Step 1. Verily that the user application program is correct.

Verify that your interrupt reaconse task contains an "EVEN I" statement that will be executed. Check carefully to determine whether a higher priority task is preventing the interrupt reaconse task from running. Make certain that the processing of your alstements agrees with the examples in section 4.5.

Step 2. Verily that the hardware is working correctly.

Verify the hardware functionality by systematically ewapping out the input module, the processor module (s), and the backplane. After each awap if the problem is not corrected, replace the original item before ewapping out the next item.

## Appendix A

## **Technical Specifications**

## **Ambient Conditions**

- Storage temperature, -40° C 85° C.
- Operating temperature: 0° C 60° C
- Itumidity: 5 90% non-condensing

## Maximum Module Power Dissipation

10 Watts

### Dimensions

- Height 11.75 inches
- Wieth: 1.25 Inches
- Depth: 7.375 inches.

### System Power Requirements

- a Volts: 1700 ma
- +12 Volta Bolma
- −12 Volts: 35 mat

## **Resolver Specifications**

- Frequency of operation, 2361 Hz
- Minimum rotor impedance: 400 Onme.
- Transformer ratio, 26(11.8)

## External Strobe Minimum Trigger Time

1 millsecond

## Appendix B

## Module Block Diagram

![](_page_38_Figure_2.jpeg)

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# Appendix C

## **Field Connections**

Pin No.	Function		Wire Color Code
1	Reference Oupput	(+)	Вгожн
7	Beterence Output	- 3 I	White/Brown Stripe
3	Beterence Input	323	Boc
4	Reference Input	(-)	White/Rec Stripe
۵	Sinc Input	(+)	Orange
6	Sine Inout	()	White/Orange Shipe
7	Gosine input	301	Yellow
ы	Cosine nput	(-)	White/Yellow Stripe
9	External Tripger	(±)	Groan
10	External Trigger	<u>}-1</u>	White/Green Stripe

# Appendix D

## **Related Components**

600123-R	- Resolver (X1) (57C360)
800123-S	<ul> <li>Resolver (X2)</li> </ul>
E00123-T	<ul> <li>Resolver (X5)</li> </ul>
200300-004	Designed for both loot-mounting and C-face mounting
\$00123-2B	- Resolver (X1) (970/861)
800123-25	- Resolver (X2)
800123-2	- Resolver (X5)
	Designed for cirect-coupling,
576378	<ul> <li>Terminal Strip/Cable Assembly</li> </ul>

This secently consists of a terminal sing, cable, and mating connector. It is used to connect field signals to the faceplate of the input module.

![](_page_42_Figure_4.jpeg)

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